L Number	Hits	Search Text	DB	Time stamp
-	232	703/20 and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 10:40
-	211	703/20 and (memory address bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 10:41
-	176	703/20 and (address bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 10:41
-	97	703/20 and (address and bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 10:41
-	95	703/20 and (memory and address and bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 10:42
-	56	703/20 and (memory and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 12:23
-	109	703/21 and (memory and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 12:51
-	108	703/22 and (memory and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 12:55
-	54	703/$ and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:15
-	6	711/210 and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:01

-	139	711/$ and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 14:18
-	128	711/$ and (memory adj model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:10
-	1	711/$ and (memory adj model with error same (address and bit)) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:20
-	1	711/$ and (memory adj model same error same (address and bit)) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:19
-	1	711/$ and (memory adj model with error) and memory same (address and bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:12
-	7	711/$ and (memory adj model same error) and memory same (address and bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:19
-	7	711/$ and (memory adj model same error) and (address with bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:17
-	63	711/$ and (memory adj model) and (memory same error) and (address with bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:14
-	50	711/$ and (memory adj model) and (memory with error) and (address with bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:15
-	2	711/$ and (memory adj model) same (memory with error) and (address with bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:17
-	9	714/33 and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:16

-	4	714/42 and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:16
-	2	714/53 and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:16
-	1	714/54 and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:17
-	99	714/$ and (memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:17
-	3	714/$ and (memory adj model) same (memory with error) and (address with bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:17
-	9	714/$ and (memory adj model same error) and (address with bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:17
-	10	714/$ and (memory adj model same error) and memory same (address and bit) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 13:19
-	1	714/$ and (memory adj model same error same (address and bit)) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 14:14
-	0	714/$ and (lsi and memory adj model same error same (address and bit)) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 14:15
-	0	(lsi and memory adj model same error same (address and bit)) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 14:15
-	3	(lsi and memory same model same error same (address and bit)) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 14:15

-	84	(lsi and memory near model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 14:18
-	76	(lsi and memory adj model and address and bit and error) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:17
-	5	error same memory same address same bit same reversal and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:19
-	607	error same memory same address and bit near (revers$2 invert$2) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:21
-	30	error same memory same address same bit near (revers$2 invert$2) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:21
-	1	703/$ and error same memory same address same bit near (revers$2 invert$2) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:22
-	1	703/$ and error same memory same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:22
-	1	711/$ and error same memory same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:22
-	16	714/$ and error same memory same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:28
-	0	714/$ and memory near model and error same memory same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:29
-	0	memory near model and error same memory same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:29

-	0	memory near model and error same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:29
-	42	memory near model and error same address and bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:30
-	6	memory near model and error same memory same address and bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:32
-	50	memory near model and error same memory same bit and bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:32
-	158	simulat$4 and error same memory same address and bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:33
-	0	simulat$4 same error same memory same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:34
-	7	simulat$4 same error same memory same address and bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:33
-	0	simulat$4 with memory and error same memory same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:34
-	0	simulat$4 with memory and error same address same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:34
-	6	simulat$4 with memory and error same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:37
-	22	model$4 with memory and error same bit near (revers$3 invert$3) and (@ad<20000911 @rlad<20000911)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/03 16:37

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10:14 |